Nonlinear Models of Microwave Power Devices and Circuits

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Abstract — This paper presents an overview of activities on RF non-linear modeling of power devices and amplifiers throughout Europe.

I. INTRODUCTION

The background of having an extensive modeling activity within the TARGET NoE [1] is the observation that in most papers, researchers show what they would like to show when they compare their model with measurements, which could mislead the reader when ignoring other significant measurements. Several efforts to validate and compare models in a systematic way were initiated [2]-[6], but those methodologies are not wellfounded and incomplete. Based on this motivation, the development of a modeling validation and comparison framework is undertaken within TARGET.

To make this approach as general as possible, several modeling techniques on both the device and circuit level are taking part in this activity. Table 2 presents an overview of the transistor modeling expertise across the TARGET partners. Table 3 presents the corresponding overview of expertise on circuit-level modeling across the TARGET partners. Modeling approaches range from physical based methods ([14]-[16]), ([29]), equivalent circuit-based and empirical methods ([17]-[25]), ([30]-[32]) to behavioral modeling methods ([24]-[28]), ([33]-[34], [13]) for devices and circuits modeling, respectively.

In this paper, we pick-out three of these modeling techniques to present a flavor of the recent developments in this field. Sections II and III cover examples of transistor modeling techniques, i.e., equivalent circuit and behavioral modeling, while Section IV presents a case of behavioral amplifier modeling.

II. TRANSISTOR EQUIVALENT CIRCUIT MODELING

Due to the requirements imposed by the new communication systems, conventional electrical equivalent circuit modeling schemes must go further and should be able to describe accurately not only harmonic content but also IMD distortion, spectral re-growth (ACPR, NPR), sweet-spot evolution, thermal dependence and self-heating, LF dispersion, optical interaction, etc.

Successive differentiation of I/V and S-parameter measurements can lead [7] to erroneous derivations of

the third order and cross derivatives which are responsible of the linearity of the device for highly linear and efficient power amplifier applications. One solution is to use continuous and derivable functions having well designed polynomial argument (e.g., the well known Angelov and Cobra models).

A second approach could be to modify in an intelligent manner any classical nonlinear model (e.g., Materka model) through the use of a kind of effective gate control voltage that assures continuity of the high order derivatives [8]. Fig.1 shows the behavior of the high order derivatives using this modification for a power FET. The evolution of the null of Gm3 in the high gain region will be a good candidate for the design of a highly linear high efficient amplifier in deep B operation.



Fig.1 High Order Derivatives of a 6 mm power MESFET

The potentiality of these continuous and derivable models can be seen in Fig.2 for a QPSK driven medium power device, where the agreement between simulation and measurement validates these approaches.



Fig.2 Output QPSK spectrum

III. TRANSISTOR BEHAVIORAL MODELING

The state-space behavioral modeling approach aims to identify a black-box dynamical description of a microwave device. The method is measurement based and makes use of large-signal vector measurements. The basic principle of the modeling method involves that the considered two-port microwave devices can be described by equations of the form:

$$I_{1}(t) = f_{1}(V_{1}(t), V_{2}(t), \dot{V}_{1}(t), \dot{V}_{2}(t), \ddot{V}_{1}(t), \ddot{V}_{2}(t), \cdots, \dot{I}_{1}(t), \dot{I}_{2}(t), \cdots)$$
(1)
$$I_{2}(t) = f_{2}(V_{1}(t), V_{2}(t), \dot{V}_{1}(t), \dot{V}_{2}(t), \ddot{V}_{1}(t), \ddot{V}_{2}(t), \cdots, \dot{I}_{1}(t), \dot{I}_{2}(t), \cdots)$$

with $I_1(t)$ and $I_2(t)$ the terminal currents, $V_1(t)$ and $V_2(t)$ the terminal voltages, and the superscript dots representing the (higher order) time derivatives. The objective of the modeling technique is to find the number of independent or state variables, and consequently to determine the functional relationships $f_1(.)$ and $f_2(.)$ by fitting the measured currents to the measured state variables.

This technique was first successfully applied to lowpower devices, such as diodes, HEMTs, and amplifiers. In Ref. [9], we studied a SiGe HBT and found that the self-heating could not be neglected. We included the dissipated power as additional independent variable in the model description and, when using an artificial neural network as fitting function, the training error had reduced by a factor of ten. In Ref. [10], we built a model for GaN HEMTs in order to have a model that is suitable for PA designs. The model was constructed from load-pull measurements. To collect data near the optimal load in an efficient way, we combined passive and active load-pull in our measurement set-up. A passive tuner was used to apply a high load (which is an optimal load condition for these devices), and excursions from this position were realized using active injection. In this way, we obtained a model that is accurate when being evaluated at loads not part of the data set for model training.



Fig. 3 Measured ('x') and state-space model simulated (circles) b_2 (=P_{out}) at f_1 (top) and b_2 at $2f_1$ - f_0 (bottom) for a three-tone excitation around 3 GHz.

So far, this state-space modeling approach was based on single-tone measurements, which is in contrast to the actual signal types being present in telecommunication systems. Therefore, we adopted the modeling procedure to be able to deal with multi-sine excitations. It is known that device characteristics may vary as function of tone spacing due to slow-memory effects. To take this into account, the model equations (1) are expanded by adding independent variables that represent time lags related to the envelope period [11]. This extended formalism was applied to a HEMT device, and modeling results are compared to measurements in Fig. 3. We notice that the tone-spacing dependency is well predicted.

IV. POWER AMPLIFIER BEHAVIORAL MODELING

Behavioral modeling of PAs is widely accomplished on the basis of quasi-static AM/AM and AM/PM input/output characteristics, which are fine for narrowband modulated signals. However, modern high-capacity multi-carrier digital radio links involve large signal bandwidths, and therefore PA models should also take into account the amplifier distortion due to both the large amplitude and the large bandwidth of the input signal.

A non-linear dynamic modeling approach based on a modified Volterra-like integral series expansion has been recently proposed [12]. According to this method, the inband complex modulation envelope b(t) of the output signal around an equivalent carrier frequency f_0 , may be expressed as:

$$b(t) = a(t)H(f_0, |a(t)|) + \int_0^{t_s} h_1(\tau_1) \cdot [a(t-\tau_1) - a(t)] e^{-j2\pi_0 \tau_1} d\tau_1 + + \int_0^{\tau_s} g_1(\tau_1, f_0, |a(t)|) \cdot [a(t-\tau_1) - a(t)] e^{-j2\pi_0 \tau_1} d\tau_1 +$$
(2)
+ $a^2(t) \int_0^{\tau_s} g_2^*(\tau_1, f_0, |a(t)|) \cdot [a^*(t-\tau_1) - a^*(t)] \cdot e^{j2\pi_0 \tau_1} d\tau_1$

Where a(t) represents the input signal complex envelope. The function H in (2) describes the non-linear memoryless contribution in the PA envelope response, which corresponds to the quasi-static AM/AM-AM/PM characteristics. According to the more general approach [12], additional integral terms, extended over the finite *memory time* T_B of the system, describe both the linear and non-linear non-quasi-static dynamic (i.e., memory related) effects. More precisely, the first integral in (2) account the purely-linear dynamic takes into contribution, while the last two integral terms describe the non-linear memory effects due to the non-negligible bandwidth of the input signal, respectively. In the presence of a constant input envelope a(t) (nonmodulated carrier) or a very narrow-band signal, all the memory-dependent contributions vanish and the corresponding output is given by the first term only (classical approaches). Instead, in the presence of a wideband modulated signal, the integrals in (2) become progressively more important as the bandwidth of a(t)increases.

The model (2) was characterized and identified for a single-ended PHEMT amplifier operating at 2 GHz. More precisely, in order to validate the behavioral model, the actual measurements to be carried out for the amplifier characterization were numerically simulated at the device-level by using the commercial CAD tool ADS. Predictions in accordance with (2) were compared to results obtained on the basis of conventional quasi-static AM/AM-AM/PM characteristics and also to accurate circuit simulations using device-level models of the same amplifier. Some of the preliminary results presented in [13] are here also shown in Table 1.

Two Tone Test Output Spectrum		
Simulation	Tones	
	-50MHz	50MHz
AM/AM-AM/PM Behav. Mod.	10.8 dBm	10.8 dBm
Device-level Mod. (Circ. Sim.)	12.4 dBm	8.9 dBm
Nonlinear Dynamic Behav. Mod.	12.4 dBm	9.2 dBm

Table 1 – Intermodulation distortion prediction ([13]).

V. CONCLUSION

This overview demonstrates the diversity in modeling approaches and the high expertise of European research groups in the field of nonlinear microwave modeling.

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REFERENCES

- The official website of TARGET NoE: http://www.targetnet.org/
- [2] The official website of Compact modeling council: http://www.eigroup.org/cmc/
- [3] J. Rodriguez-Tellez *et al.*, "Comparison of nonlinear MESFET models for wideband circuit design," *IEEE Trans. Electron Devices*, vol.41, no.3, 1994
- [4] E. Sijercic and B. Pejcinovic, "Comparison of non-linear MESFET models," *ICECS Conf.*, 2002
- [5] J. Castelino *et al.*, "Comparison of non-linear MESFET models over 1-12 GHz frequency range," *ICECS Conf.*, 2003
- [6] M. Miller *et al.*, "Choosing an optimum large signal model for GaAs MESFETs and HEMTs," *Intern. Microwave Symp.*, 1990
- [7] J.C. Pedro and J. Perez, "Accurate simulation of GaAs MESFET's intermodulation using a new drain-source current model," *IEEE Trans. MTT*, vol.42, no.1, 1994
- [8] J.A. García *et al.*, "Resistive FET mixer conversion loss and IMD optimization by selective drain bias," *IEEE Trans. MTT*, vol.47, no.12, 1999
- [9] D. Schreurs *et al.*, "ANN model for SiGe HBTs constructed from time-domain large-signal measurements," *GAAS Symp.*, 2002
- [10] D. Schreurs et al., "ANN Model for AlGaN/GaN HEMTs Constructed from Near-Optimal-Load Large-Signal Measurements," *Intern. Microwave Symp.*, 2003
- [11] D. Schreurs *et al.*, "State-Space Modelling of Slow-Memory Effects Based on Multisine Vector Measurements," *ARFTG Conf.*, pp. 81-87, 2003
- [12] D. Mirri *et al.*, "A nonlinear dynamic model for performance analysis of large-signal amplifiers in communication systems," *IEEE Trans. IM*, vol.53, no.2, 2004
- [13] C. Florian *et al.*, "CAD identification and validation of a non-linear dynamic model for performance analysis of large-signal amplifiers," *Int. Microwave Symp.*, 2003
- [14] M. Gargalakos et al., "A Generalized Three Dimensional Non-Linear Analysis of MMIC Geometries," *Electromagnetics Journal*, vol.22, no.3, 2002

- [15] A. Santarelli et al., "Equivalent-Voltage Approach for Modeling Low-Frequency Dispersive Effects in Microwave FETs," *IEEE Microwave and Wireless* Components Letters, vol.12, no. 9, 2002
- [16] M. Berroth et al., "Advanced Large-Signal Modeling of GaN-HEMTs," IEEE Lester Eastman Conf. on High Performance Devices, 2002
- [17] C. Curras *et al.*, "Direct extraction of non-linear FET Q-V functions from time domain large signal measurements," *IEEE MGWL*, vol.10, no.12, 2000
- [18] J.G. Tartari *et al.*, "Optimizing SiGe HBTs technology using small-signal and high frequency noise device's modeling," *MRS Spring meeting*, 2004
- [19] I. Angelov, "HFET and HBT Modelling for Circuit Analysis," *IEICE Trans. Electronics*, vol.E86, no.10, 2003
- [20] M. Lazaro *et al.*, "Smoothing the Canonical Piecewise Linear Model: An Efficient and Derivable Large Signal Model for MESFET/HEMT Transistors," *IEEE Trans. Circuits and Systems Part 1*, vol.48, no.2, 2001
- [21] A. Cidronali *et al.*, "A Scalable PHEMT Model Taking Into Account Electromagnetic and Electro-Thermal Effects," *GAAS Conf.*, 2003
- [22] C. Camacho-Peñalosa *et al.*, "A single relaxation-time non-quasi-static model for MESFETs and HEMTs," *ISMOT Symp.*, 2003
- [23] M. Fernandez et al., "A simplified broad-band large-signal nonquasi-static table-based FET model," *IEEE Trans. MTT*, vol.48, no.3, 2000
- [24] P.M. Cabral *et al.*, "A Nonlinear Device Model of Microwave Power GaN HEMTs for High-Power Amplifier Design," *IEEE Trans. MTT*, 2004, in press.
- [25] D. Schreurs *et al.*, "Straightforward and accurate nonlinear device model parameter estimation method based on vectorial large-signal measurements," *IEEE Trans. Microwave Theory Techn.*, vol. 50, no. 10, 2002
- [26] A. Costantini et al., "Accurate prediction of PHEMT intermodulation distortion using the nonlinear discrete convolution model," *Intern. Microwave Symp.*, 2002
- [27] F. Giannini *et al.*, "Modeling power and intermodulation behavior of microwave transistors with unified smallsignal/large-signal neural network models," *Intern. J. RF and Microwave Computer-Aided Eng.*, vol.13, no.4, 2003
- [28] A. Samelis, "Modeling the Bias Dependence of the Base-Collector Capacitance of Power Heterojunction Bipolar Transistor," *IEEE Trans. MTT*, vol.47, no.5, 1999
- [29] R. Makri *et al.*, "Computation of Passive Finite Three Dimensional MMIC Structures using a Global Method of Moments Approach," *J. Electromagnetic Waves and Applications*, vol.16, no.2, 2002
- [30] S. Long *et al.*, "Ka-band coplanar low-noise amplifier design with power PHEMTs," *GAAS Symp.*, 2003
- [31] E. Malaver et al., "Improving the Linearity-Efficiency Trade-off in FET power amplifiers using Large Signal IMD sweet-spots," *Microwave and Optical Technology Letters*, vol.41, no.4, 2004
- [32] T. Fowler *et al.*, "Efficiency Improvement at Low Power Levels for Linear CDMA and WCDMA Power Amplifiers," *RFIC Symp.*, 2002
- [33] D. Schreurs *et al.*, "Construction of behavioural models for microwave devices from time-domain large-signal measurements to speed-up high-level design simulations," *Intern. J. RF and Microwave Computer Aided Eng.*, vol.13, no.1, 2003
- [34] A. Cidronali *et al.*, "Extraction of Conversion Matrices for P-HEMTs based on Vectorial Large Signal Measurements," *Intern. Microwave Symp.*, 2003

Partners	Modeling Focus
TUWien	- Physical modeling of HEMT's, HBT's, and MOSFET's, with the simulation modes: DC, S-
	parameter, large-signal transient, and thermal interaction (2D and 3D).
NTUA [14]	- Device models based on electromagnetic simulations
Univ. Ferrara [15]	- Models for low-frequency dispersion: Back gating and equivalent voltage approaches.
	- Distributed models based on electromagnetic simulations
INT [16]	- Direct extraction based modeling techniques for FETs, with special attention to low-frequency
	dispersion effects, self-heating in case of high-power devices, e.g., GaN HEMTs, and scalability
	- Model parameters are physical based, such that model is attractive not only for circuit design
	engineers, but also for the scientists involved into the technology optimization
Univ. Cardiff [17]	- Table-based models employing single-tone large-signal measurements including harmonic load-
	pull, and two-tone large-signal measurements, including load-pull at IF frequencies
IEMN	- Equivalent circuit modeling using pulsed DC and S-par. meas., to be well correlated with
	devices' technology (small and large band gap devices)
IRCOM	- Modeling using pulsed large-signal measurements
	- Modeling of non-linear stability
LAAS [18]	- Low-frequency noise and high frequency noise modeling (with electrical small-signal
	parameters) used for process improvement and (non-)linear circuit design (narrow and wide band
	gap devices)
Chalmers Univ.[19]	- Empirical large-signal and thermal noise modeling of MOSFETs, HEMTs, and HBTs
Cantabria Univ.	- Empirical and Neural based large-signal modeling of FETs, HEMTs with focus on IMD behavior
[20]	from DC, S, Pulsed I/V and High Order Derivatives Measurements
IMST	- Equivalent circuit based modeling, with focus to power measurements
MIDRA [21]	- Nonlinear device models combining electrical, thermal and electromagnetic simulations
Univ. Malaga [22]	- Distributed and scalable models
	- Models paying special attention to: non-quasi-static effects
Univ. Vigo [23]	- Table-based non-quasi-static non-linear FET modeling from DC & S-parameter measurements up
	to 120 GHz, and from large signal waveform measurements
	- Small- and large-signal modeling of HBTs
Univ. Aveiro [24]	- Compact models supported by nonlinear equivalent circuits
	- Volterra series based models
K.U.Leuven [25]	- Equivalent circuit based modeling
	- State-space modeling using large-signal vector measurements
Univ. Bologna [26]	- Non-linear discrete-convolution models
	- General-purpose multi-bias parasitic identification
Tor Vergata [27]	- Device modeling using artificial neural networks, with special attention to the higher-order
Their Timestal	derivatives of the current
Univ. Limerick	- Device instantaneous Fourier series and complex Bessel function series approximation models
II. Out [20]	- Extraction of benavioral characteristics under specific system and signal configurations
Univ. Crete [28]	- Electro-thermal modeling for power transistors

Table 2: Overview of device-level modeling approaches at European Institutes.

Partners	Modeling Focus
TUWien	- Mixed mode analysis based on physical models and (external) compact models with the
	simulation modes: DC, S-parameter, large-signal transient, and thermal interaction (2D and 3D).
NTUA [29]	- Amplifier models making use of electromagnetic simulations
LAAS [30]	- Low-frequency noise modeling, phase noise modeling for non-linear low phase noise
	applications (MIC or MMIC VCOs,) + low-noise amplifiers design
Chalmers Univ.	- Volterra-series based modeling for distortion and low frequency dispersion
Cantabria Univ.	- Modeling of distortion and low-frequency dispersion by large-signal measurements.
[31]	- Empirical large-signal modeling.
Univ. Crete [32]	- Electro-thermal modeling for amplifiers
INT	- Direct extraction based modeling techniques, with special attention to self-biasing, dynamic
	breakdown voltage, and possible deviations between optimum load conditions deduced from
	static measurements and dynamic ones. Also, thermal effects and packaging aspects.
	- Modeling using large-signal measurements.
Univ. Aveiro	- Volterra series based modeling
K.U.Leuven [33]	- Behavioral modeling using large-signal vector measurements
Univ. Cardiff	 Modeling using harmonic-load pull large-signal measurements
MIDRA [34]	- Modeling of distortion and low-frequency dispersion by large-signal measurements
	- Modeling of large-signal stability
Univ. Malaga	 Modeling developments related to harmonic-balance based simulation techniques
Tor Vergata	- Behavioral modeling using power measurements
Univ. Limerick	- Amplifier instantaneous Fourier series and complex Bessel function series approximation models
	- Extraction of behavioral characteristics under specific system and signal configurations
Univ. Bologna [13]	- Behavioral modeling by means of Volterra-like integral approaches

Table 3: Overview of circuit-level modeling approaches at European Institutes.