# Thermal Analysis of RF-MEMS Switches for Power Handling Front-end

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An experimental setup for the Abstract characterization of electromagnetic induced heat on MEMS devices undertaking high RF power regime (> 5W) is here proposed. The technique is based on infrared (IR) imaging of on-probe DUT, while it is in working conditions. The measured temperature distributions, for different working state of a RF-MEMS switch, are given. The results show that for a first considered capacitive switch, the most critical working state is the OFF-state (membrane actuated). In this case the hot-spots temperature reach 75.5°C, for a input power of 6.3 W at 10GHz. On the other hand, for the same incident power and frequency a maximum rise of only 5°C has been measured for the ONstate (membrane in the rest position). Temperature mapping results for a second switch design are moreover presented. This steady-state map offers a real time global performance overview of the RF induced phenomena, and represents a very valuable real-time investigation tool for integrated MEMS and RFIC power handling front-end.

## I. INTRODUCTION

Use of novel polymer based technology is emerging as very promising solution for integration on chip of wireless front-end. The excellent electric properties are however compromise by the low thermal conductivity, which represents a bottleneck in using such materials for high power handling (above 5W). The lack of heat sink could yield serious power handling limits, as thermomechanical deformation. This phenomenon becomes very serious in case of very compliant devices as MEMS varactors and switches, sensitive to the minimum displacements. Several efforts have been addressed to avoid power handling limitations by introducing separate actuation pads, or double control electrode, but very few have been dedicated to the experimental characterization [1]. So far RF induced heat due to joule effect, has been analyzed for RF-MEMS switches by simplified analytical and numerical models [2, 3]. A framework for the electromagnetic induced heat characterization by means of an experimental setup, which allows one to identify and quantify critical temperature rise (hot spots), can be a very useful tool for drawing power handling considerations and exploit them for design optimization. A characterization tool able to provide reliable experimental results directly on working devices could enable a very fast analysis of large systems as integrated front-end. The shrinking down (to micrometer scales) of reconfigurable antennae, phase shifters and switching networks, embedding MEMS functionalities, and bearing always higher RF signal power, could be investigated in real-time by means of the technique here reported.



Fig. 1. Schematic of the setup used for the thermal imaging of RF-MEMS devices under working power handling conditions.

#### **II. EXPERIMENTAL SETUP**

The measurement set-up is composed of a RF source, delivering an RF signal which power can be vary from -27 dBm up to -0.5 dBm, followed by a solid state power, amplifier with 35dB of gain in the frequency range 9.5-10.5 GHz. The maximum delivered power with the given source is of 39 dBm on 50 Ohms. Due to cable loss however, the maximum incident power at the DUT is reduced to 6.3 W. An isolator protects the amplifier in case of mismatching from spurious reflections along the signal path. A 30 dB attenuator and a power meter, with sensitivity 1 pW-100mW, constitutes the receiving part of the measurement setup. The DUT, mounted between the isolator and the attenuator, can be any device with GSG probe access. The devices are realized on silicon wafer hold by a thermo-stabilized chuck, kept at the constant temperature of 23°C. The thermal imaging is provided by the IR camera Jade MW [4], mounted on top of the probe station. The camera is characterized by a very cold detector, (93K) allowing very high thermal sensitivity. This is a critical parameter especially in case of very low emission material as gold (which emissivity ranges between 0.02 and 0.04, depending whether it is polished or electroplated). The camera works in the spectral range of 2-5.2 µm, and in the temperature range -20°C to 1300°C. A special carrier makes it free of x-y movements above the entire area of the wafer sample. A customized objective with focal length of 30 mm enables a resolution down to  $20x20-\mu m^2$  pixel (four times the estimated wavelength of the IR radiation). The IR camera is PC-assisted in order to carry out the numerical calibration and the results post processing. The entire measurement set up is shown in Fig.1.

Working State	Temperature Rise P <sub>in</sub> =3.6W				<i>Temperature Rise P<sub>in</sub>=6.3W</i>			
	Point 1	Point 2	Point 3	Point 4	Point 1	Point 2	Point 3	Point 4
ON (Up)	2.5	2.5	2.4	2.4	5	3.6	5	5
OFF (Down)	11.3	26	10	6	26.5	52.5	11.5	8.5

 TABLE I

 Temperature Rise at Steady State and at the Referenced Points (see Fig. 2)



Fig. 2. Picture of the analyzed capacitive MEMS switch. The reference point for the temperature read out are shown along with the direction of incident RF signal.

The RF-MEMS device under test consists in a capacitive shunt switch,. The device is realized in gold on Silicon and Benzocyclotene Butene, (Si/BCB, 400/20 $\mu$ m) wafer, [5]. In Fig. 2 is given the picture for the analyzed device, indicating the direction of the incident RF power and the four points taken as reference and whereupon the temperature measurement have been carried out. Although the IR camera provides the map for the temperature over the entire region, the selection of few points is required for an optimal emissivity calibration.

#### **III. MEASUREMENTS RESULTS**

## A. Temperature Time Transient

The measurements results for both working conditions (ON-OFF States), are given in Tab. I. The measurements have been performed at a frequency of 10GHz, and by switching the incident RF power from 0.03 to 6.3W. For the device in ON-state (membrane up in rest position), the RF power signal transits through the MEMS discontinuity, with an overall maximum power loss given by

$$L_{ON} = P_{in} \left( 1 - |S_{110N}|^2 - |S_{210N}|^2 \right) = 154 mW, \quad (1)$$



Fig. 3. Transient behaviour of the temperature rise for the four considered points. The RF incident power is turned from a minimum of 0.04 W to maximum of 6.3W in correspondence of *lsec*. The device is at room temperature of 23°C.

computed for a *Pin=6.3W*, and the measured scattering parameters at 10GHz,  $|S_{110N}| = -16.8dB$ , and  $|S_{210N}| = -0.2dB$ . The heat induced by Joule effect yields a temperature rise which reaches only 5°C, quite uniformly all over the observed domain (only 1.4°C of difference between the considered points). The reference point temperatures are provided for two values of incident power (3.6W and 6.3W).

In case of working condition in OFF-state, (membrane actuated down), the loss due to the MEMS discontinuity and computed similarly to (1), are  $L_{OFF} = 532mW$ , (for the same incident power level Pin=6.3W and for measured scattering parameters at 10GHz, equal to  $|S_{110FF}| = -0.4$ dB, and  $|S_{210N}| = -22dB$ ). From the measurement results it is evident (see Tab I and Fig. 3) that the maximum temperature rise takes place in correspondence of the point 2, where the increment of temperature is of 52.5°C. This can be justified by considering that the current distribution, responsible of the induced heat, has a maximum in correspondence of the membrane middle point (Point 2 see Fig. 2). It is there indeed that the capacitive virtual short takes place. The correspondent transient behaviour, showing the temperature evolution of the four considered points in time, once the input power is switched from 0.04 to 6.3W is given in Fig. 3.

Worth noting is that Point 3 and Point 4, corresponding to two symmetric positions on the gold membrane, present measured temperatures which differ for  $4^{\circ}$ C and  $3^{\circ}$ C each other, for 3.6W and 6.3W.



Fig. 4. *ON-state (Membrane up)* temperature map for the thermal steady-state in case  $P_{in}=6W$  for the. The shape of the CPW line and of the membrane are denoted by the dashed and continuous line respectively. The maximum temperature is  $45.2^{\circ}C$ .

respectively. This is likely due to the fact that in reality the structure presents slight asymmetries due to fabrication or actuation conditions, which yield the clearly observed temperature unbalance.

From this experimental observation can be drawn the conclusion that for the tested MEMS device the OFFstate (actuated membrane) results the most critical in terms of power handling. This is the opposite conclusion with respect to other research groups working on similar structure but different technology (similar devices but realized on Si/SiO<sub>2</sub>) [2]. A possible explanation of this divergence is attributed to the critical role of material thermal properties. In the device here considered, the use of 20µm of BCB buffer layer between the silicon substrate and the metal structure yields a strong thermal isolation (the thermal conductivity of BCB is 0.29 W/m K, while the Silicon's is 148 W/m K). In case of higher power loss in the MEMS region (as for switch in OFF-State or membrane down), this impedes heat drain into the bulk substrate and causes temperature rise. MEMS directly built on Silicon, on the other hand suffer less of this limit. This suggests that improvement of the thermal drain through the BCB is necessary if we want to keep the device cool.

# B. Temperature map at steady state

Once the DUT has reached the thermal steady state temperature distribution has been measured all over the region of interest (MEMS region). In figure Fig. 4 and Fig. 5, the results the two working states of a second MEMS switch design (but with same technology as the previous one), are shown. The most critical working state is the OFF-state with a maximum temperature of 84.5 °C reached on the inner conductor in proximity of the membrane (which is in down in actuated position), on the side where the power is injected (top side in the figure). This confirms the previous measurements results.



Fig. 5. *OFF-state (membrane down) temperature map for the thermal steady-state in case of*  $P_{in}$ =6W. The shape of the CPW line and of the membrane are denoted by the dashed and continuous line respectively. The maximum temperature is 84.5°C.

Indeed the temperature distribution for the ON-state is more symmetric with respect of the signal flow and the maximum value does not go beyond 45.2°C. From the temperature maps it become quite evident that very useful information as the location of the hot spots can be easily identified. The experimental results have been validated with a numerical modelling based on commercial tools (HFSS and e-physics from Ansoft Corp.) showing a very close agreement.



Fig. 6. *OFF-state (membrane down) temperature map modelling results for input power*  $P_{in}$ =6W. The scale for the different metal parts are kept separate for the sake of clearness. The maximum temperature in the MEMS region is 82.6°C.

This are based on the FEM method and gives the possibility to cope the electromagnetic problem into the thermal one, by reusing the power loss density due to conductor and dielectric derived from the full-wave electromagnetic solution [6]. For the ON and OFF-state working states the simulated temperature map a very similar qualitative distribution and maximum temperature of 56.3°C and 82.5°C. Considering the difficulty in

modelling the RF induced thermal problem for these device the results can be considered promising. The results for the only OFF-state are shown in Fig. 6 (the rise of temperature on the signal line at the input section is likely due to excitation fields).

## **IV. CONCLUSIONS**

In this paper an experimental setup which allows one to carry out thermal investigation of RF induced heat phenomena has been presented. The method enables the real-time investigation of RF-MEMS (so as RFICs) while under severe power regime working conditions.

The results for a capacitive MEMS switch have shown that for 6.3W of RF incident signal at 10GHz, the temperature rise reaches the 75.5°C in case of switch in OFF state, while remains below 28°C in the ON-State for the same input signal. In both cases no electromagnetic performances degradation have been observed. However because of the linear proportionality between incident RF power and temperature [2], thermo-mechanical failures may be expected for the analysed device working in the OFF-State, and for input power above 10W. In this case, the hot spots temperature is expected to reach temperatures above 120°C. Power handling design optimization could strongly benefit with the experimental insight provided by the presented technique. Especially in case of integrated Microsystems and front end, based on polymer technology (as BCB, Liquid crystal polymers or polyamide), real-time thermal imaging give immediate feedback on possible RF induced unwanted phenomena and represents immediate investigation tool for rapid failures inspection of planar MEMS so as RFIC devices.

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